

Matching of cost of implementation (among macrocells) combinatorial circuits synthesized with the help of MAX+PLUS package II of Altera (C_A) corporation and at usage of method M2 of synthesis of single-level combinatorial circuits with usage of mounting connection of outputs PLD on OR ZUBR (C_2) package for PLD of FLEX 10K set

Name	L	N	P	C_A	C_2	C_A/C_2
9sym	9	1	87	87	59	1,48
B12	15	9	431	24	20	1,20
Cps	24	109	654	1484	751	1,98
Inc	7	9	34	40	36	1,11
Pdc	16	40	1280	534	164	3,26
Z9sym	9	1	420	109	59	1,85
mid						1,81