

Matching of cost of implementation (among macrocells) combinatorial circuits synthesized with help MAX+PLUS II of Altera (C_A) corporation and at usage of method M2 of synthesis of single-level combinatorial circuits with usage of mounting connection of outputs PLD on OR ZUBR (C_2) for PLD of MAX 7000 set

Name	L	N	P	C_A	C_2	C_A/C_2
9sym	9	1	87	20	15	1,33
Alu4	14	8	1028	143	116	1,23
Apex3	54	50	280	316	168	1,88
Apex4	9	19	438	252	220	1,15
Ex1010	10	10	1024	239	182	1,31
Z9sym	9	1	420	22	15	1,47
mid						1,40